

Single-Mode 40GBASE-LR4 QSFP+ Transceiver RoHS6 Compliant

Features

- ü Compliant to the IEEE 802.3ba(40GBASE-LR4)
- ü Compliant to the QSFP+ MSA SFF-8436 Specification
- ü Up to 10km over SMF
- ü DFBs and PIN monitor photodiodes array for transmitter section
- ü PIN detectors and TIAs array for receiver section
- ü Four 10Gbps CWDM channels in the 1300nm band
- ü I2C interface with integrated Digital Diagnostic Monitoring
- ü Utilizes two standard LC optical connector
- ü Operating Case Temperature: -10°C~+70°C
- ü 3 Year warranty

Applications

- ü 40GBASE-LR4 Ethernet links
- ü Infiniband QDR and DDR interconnects Client-side
- ü 40G Telecom connections

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	Ts	-40	+75	°C
Supply Voltage	Vcc	-0.5	3.6	V
Operating Relative Humidity	RH	5	85	%

Exceeding any one of these values may destroy the device immediately

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	Tc YQS40-3110	-10		70	°C
Power Supply Voltage	Vcc	3.15	3.3	3.45	V
Power Supply Current	Icc			1000	mA
Power Dissipation	Pd			3.5	W
Aggregate Bit Rate	BR _{AVE}		41.25		Gbps
Lane Bit Rate	BR _{LANE}		10.3125		Gbps

Regulatory Compliance

Feature	Standard	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883G Method 3015.7	Class 1C (>1000 V)
Electrostatic Discharge to the enclosure	EN 55024:1998+A1+A2 IEC-61000-4-2 GR-1089-CORE	Compliant with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022:2006 CISPR 22B :2006 VCCI Class B	Compliant with standards Noise frequency range: 30MHz to 6GHz. Good system EMI design practice required to achieve Class B margins. System margins are dependent on customer host board and chassis design.
Immunity	EN 55024:1998+A1+A2 IEC 61000-4-3	Compliant with standards. 1KHz sine-wave, 80% AM, from 80MHz to 1GHz. No effect on transmitter/receiver performance is detectable between these limits.
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1:2007 EN (IEC) 60825-2:2004+A1	CDRH compliant and Class I laser product. <u>TüV</u> Certificate
Component Recognition	UL and CUL EN60950-1:2006	UL <u>TüV</u> Certificate (CB scheme)
RoHS6	2002/95/EC 4.1&4.2 2005/747/EC 5&7&13	Compliant with standards ^{†note}

Performance Specifications - Electrical

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Transmitter Section:						
Single ended input voltage tolerance		-0.3		4	V \square	Referred to TP1 signal common \square
AC common mode input voltage tolerance		15			mV	RMS
Input Impedance (Differential)	Z _{in}	85	100	115	ohms	R _{in} > 100 kohms @ DC
TX Disable	Disable	V _{IH}	2	V _{CC} +0.3	V	
	Enable	V _{IL}	0	0.8		
TX FAULT	Fault	V _{OH}	2.4	V _{CC} +0.3	V	
	Normal	V _{OL}	0	0.5		
Receiver Section:						
Single ended output voltage		-0.3		4	V \square	Referred to signal common \square
AC common mode voltage				7.5	mV	RMS
Termination mismatch at 1MHz				5	%	
Output Impedance (Differential)	Z _{out}	85	100	115	ohms	
Output Rise/Fall Time	t _r /t _f	30			ps	10%~90%
RX_LOS	LOS	V _{OH}	2.4	V _{CC} +0.3	V	
	Normal	V _{OL}	0	0.8	V	

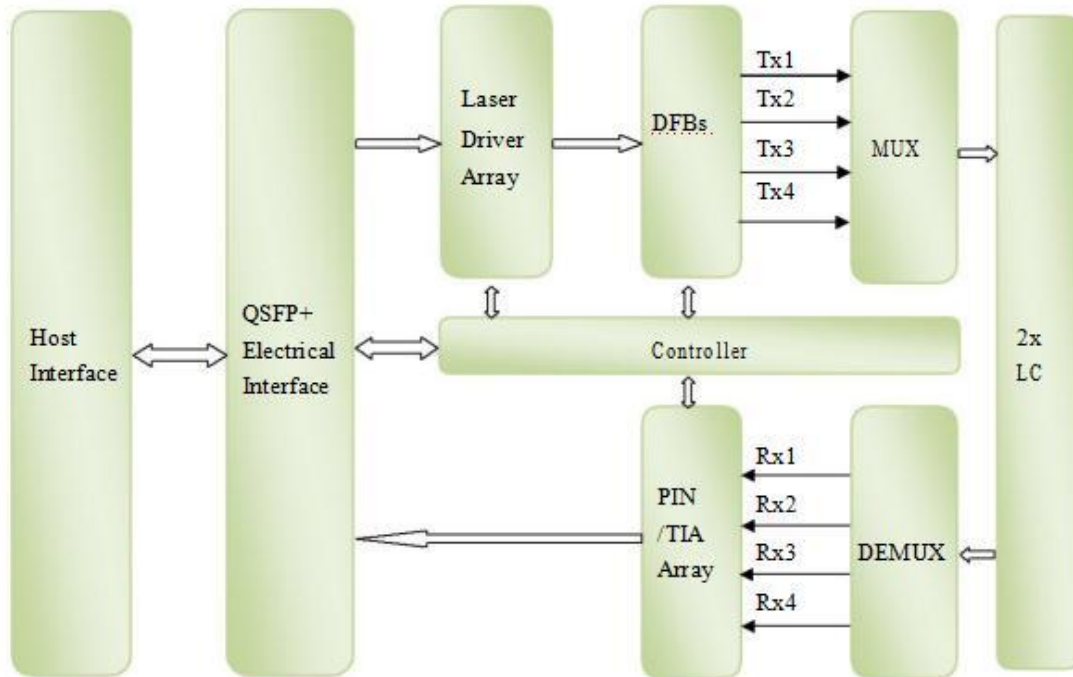
Optical and Electrical Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit
SMF	L	-	10	-	km
Aggregate Bit Rate	BR _{AVE}	-	41.25	-	Gbps
Per Lane Bit Rate	BR _{LANE}	-	10.3125	-	Gbps
Transmitter Section:					
Center Wavelength	λ_c	1264.5	1271	1277.5	nm
		1284.5	1291	1297.5	
		1304.5	1311	1317.5	
		1324.5	1331	1337.5	
-20dB spectral width	$\Delta\lambda$	-	-	1	nm
Average Launch Power, Each Lane ^{*(note1)}	P _{out/lane}	-7	-	2.3	dBm
Transmit OMA, per Lane	TX_OMA/lane	-4	-	3.5	dBm
Extinction Ratio	ER	3.5	-	-	dB
Output Optical Eye ^{*(note2)}	IEEE 802.3ba-2010 Compliant				
Receiver Section:					
Center Wavelength	λ_c	1264.5	1271	1277.5	nm
		1284.5	1291	1297.5	
		1304.5	1311	1317.5	
		1324.5	1331	1337.5	
Damage Threshold		3.3	-	-	dBm
Stressed receiver sensitivity in OMA, each lane	P _{mins}	-	-	-11.5	dBm
Maximum Receive Power, each lane	P _{max}	2.3	-	-	dBm
Receiver Reflectance	R _r	-	-	-26	dB
LOS De-Assert	LOSD	-	-	-11.5	dBm
LOS Assert	LOSA	-20	-	-	dBm

Notes:

1. Output is coupled into a 9/125 μ m Single-Mode fiber.
2. Filtered, measured with a PRBS 231-1 test pattern @10.3125Gbps

Functional Description of Transceiver



QSFP+ Transceiver Electrical Pad Layout

38	GND
37	TX1n
36	TX1p
35	GND
34	TX3n
33	TX3p
32	GND
31	LPMODE
30	Vcc1
29	VccTx
28	IntL
27	ModPrsL
26	GND
25	RX4p
24	Rx4n
23	GND
22	RX2p
21	RX2n
20	GND

Top Side
Viewed From Top

Module Card Edge

GND	1
TX2n	2
TX2p	3
GND	4
TX4n	5
TX4p	6
GND	7
ModseL	8
ResetL	9
VccRx	10
SCL	11
SDA	12
GND	13
RX3p	14
Rx3n	15
GND	16
RX1p	17
RX1n	18
GND	19

Bottom Side
Viewed From Bottom

Pin Arrangement and Definition

Pin	Logic	Symbol	Description	Plug Sequence	Notes
1		GND	Ground	1	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	3	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	3	
4		GND	Ground	1	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	3	
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	3	
7		GND	Ground	1	1
8	LVTTL-I	ModSelL	Module Select	3	
9	LVTTL-I	ResetL	Module Reset	3	
10		VccRx	+3.3V Power Supply Receiver	2	2
11	LVC MOS- I/O	SCL	2-wire serial interface clock	3	
12	LVC MOS- I/O	SDA	2-wire serial interface data	3	
13		GND	Ground	1	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	3	
15	CML-O	Rx3n	Receiver Inverted Data Output	3	
16		GND	Ground	1	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	3	
18	CML-O	Rx1n	Receiver Inverted Data Output	3	
19		GND	Ground	1	1
20		GND	Ground	1	1
21	CML-O	Rx2n	Receiver Inverted Data Output	3	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	3	
23		GND	Ground	1	1
24	CML-O	Rx4n	Receiver Inverted Data Output	3	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	3	
26		GND	Ground	1	1
27	LVTTL-O	ModPrsL	Module Present	3	
28	LVTTL-O	IntL	Interrupt	3	
29		VccTx	+3.3V Power supply transmitter	2	2
30		Vcc1	+3.3V Power supply	2	2
31	LVTTL-I	LPMODE	Low Power Mode	3	
32		GND	Ground	1	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	3	
34	CML-I	Tx3n	Transmitter Inverted Data Input	3	
35		GND	Ground	1	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	3	
37	CML-I	Tx1n	Transmitter Inverted Data Input	3	

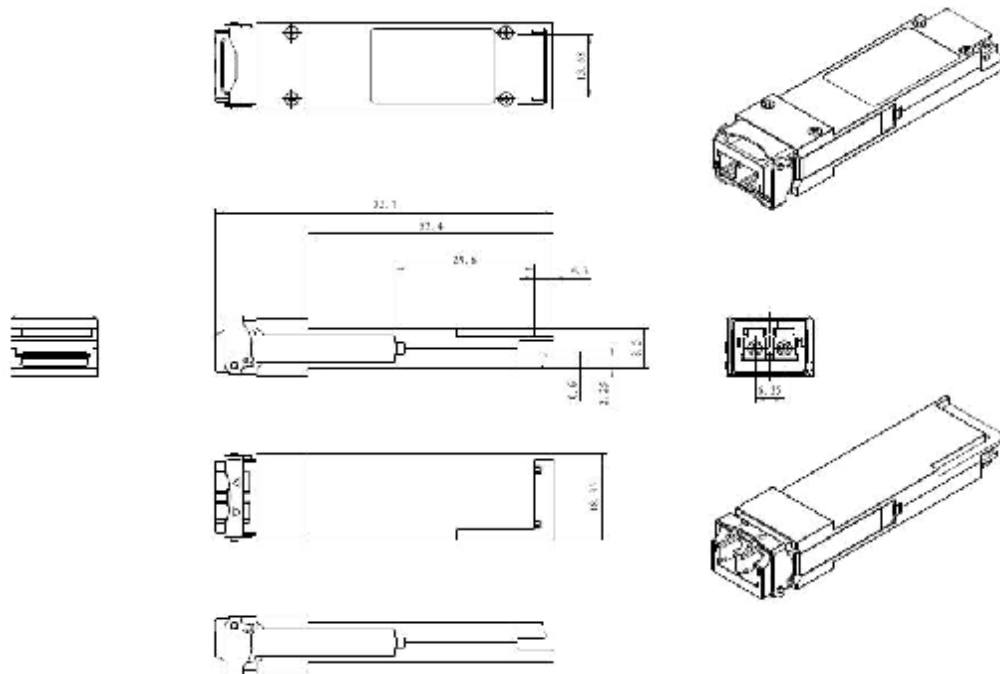
38		GND	Ground		1	1
----	--	-----	--------	--	---	---

Notes:

1: GND is the symbol for signal and supply (power) common for the QSFP+ module. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.

2: Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power supplies and shall be applied concurrently. Requirements defined for the host side of the Host Edge Card Connector are listed in Table 6. Recommended host board power supply filtering is shown in Figures 3 and 4. Vcc Rx Vcc1 and Vcc Tx may be internally connected within the QSFP+ Module in any combination. The connector pins are each rated for a maximum current of 500mA.

Mechanical Specifications



Ordering information

Part Number	Data Rate	Fiber	Distance	Interface	Temp	DDMI
BSD-QSFP-40G-LR4	40Gbps	SMF	10Km	LC	-10 to +70°C	Yes

Important Notice

Performance figures, data and any illustrative material provided in this data sheet are typical and must be specifically confirmed in writing by BSD Networks before they become applicable to any particular order or contract. In accordance with the BSD Networks policy of continuous improvement specifications may change without notice. The publication of information in this data sheet does not imply freedom from patent or other protective rights of BSD Networks or others. Further details are available from any BSD Networks sales representative.

Contact Information

BSD Networks

Phone -> 905-669-6613 x2001

Fax -> 905-669-6614

Toll Free -> 800-668-LANS (5267)

Email -> robert@bsdnetworks.com

Web: www.bsdnetworks.com

Skype: robert_bsd